



Docket No.: H6808.0004/P004
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Atsushi Takane, et al.

Examiner: Phillip A. Johnston

Application No.: 10/082,286

Art Unit: 2881

Filed: February 26, 2002

For: SEMICONDUCTOR INSPECTION SYSTEM

CONFIRMATION OF TELEPHONIC CONFERENCE WITH EXAMINER
JOHNSTON ON APRIL 7, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicants' undersigned representative acknowledges with appreciation the telephonic conference conducted with the Examiner on April 7, 2005. In the April 7, 2005 telephonic conference, the Examiner stated that there was a double-patenting issue with respect to U.S. patent application no. 10//365,383, filed on February 13, 2003. The Examiner indicated that the above-identified U.S. patent application would be in condition for allowance upon the filing of a Terminal Disclaimer.

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Consequently, a Terminal Disclaimer is being filed concurrently herewith, disclaiming U.S. patent application no. 10//365,383, filed on February 13, 2003. Favorable action on the above-identified U.S. patent application is respectfully solicited.

Dated: April __, 2005

Respectfully submitted,

By 

Mark J. Thronson

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